
Agilent Ads 2013 _HOT_ Crack

all graphene samples of this work were cvd-synthesized using a home-built cvd system based on a hot-wall reactor (details are shown in the methods and materials section). graphene samples were transferred to a silicon wafer substrate using a gentle tape delamination method, which is different from a standard scotch tape delamination method. previous studies have shown that the tape delamination method can be implemented as a high-throughput method to obtain large quantities of high-quality suspended graphene 27. to prove the scalability of our graphene fabrication, we test the transfer yield rate of three silicon wafers and three tapes. the transfer rate measured by the weight difference after and before the transfer, which is related to the transfer yield rate, is more than 60%. the graphene transferred onto the substrate was mechanically exfoliated with a special scotch tape (nitto boughts) using the dry-transfer method shown in fig. 1. the obtained graphene flake was transferred to a porous h-terminated sio₂/si substrate using the tape delamination method. to improve the bonding strength between the graphene and the sio₂ substrate, an fe thin layer (0.5 nm) was thermally evaporated on the sio₂ substrate prior to the graphene transfer. the graphene flake on the porous sio₂/si substrate was then transferred to a silicon substrate with a polymer as the sacrificial layer by a simple tape delamination method. the polymer layer was etched and removed by acetone to suspend graphene. the graphene flake was covered by an sio₂ layer on the si substrate. to eliminate the sio₂ layer on the graphene flake, the sio₂/si sample was immersed in buffered hydrofluoric acid solution (bhf) for 45 s. the edges of the graphene flake were then exfoliated by a tape delamination. after the tape delamination, the graphene flake was transferred to a mica substrate (fig. 1).

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